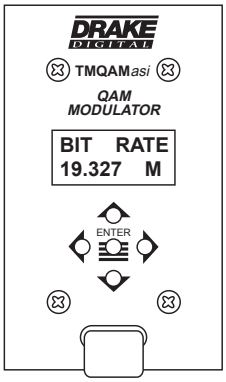
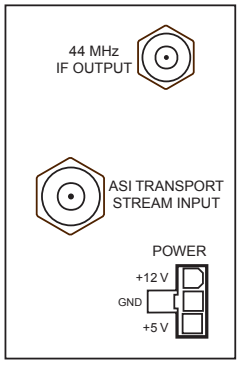
TMQAM-asi Digital QAM Modulator



Front Panel



Rear Panel

Specifications

Output

Output Frequency: 44 MHz.
Output Level: +30 dBmV.
Output Impedance: 75 Ohms.
Spurious Outputs: -55 dBc typical, in band or in adjacent channels. -60 dBc otherwise when used with Drake DUC550 or DUC860 upconverter modules.
MER: 38 dB minimum.

Modulation

Mode: 16, 32, 64, 128, 256 QAM.
Symbol Rate: 7.0 Msymbols/s Max.
Excess Bandwidth: 15% (ITU-A), 18% (ITU-B).

FEC Encoding

FEC Modes: DVB/DAVIC (ITU-T J.83 Annex A), Digicipher® II (ITU-T J.83 Annex B).

Input

Transport Stream: Serial input according to DVB ASI specifications

Connector: BNC, 75 Ohms.

General

DC Power Required: 5 VDC @ 500 mA.
Size: 2.06" W x 3.5" H x 9.25" D (5.23 cm W x 8.9 cm H x 23.5 cm D).
Weight: 1 lb. 2 oz. (0.51 Kg).

The TMQAM*asi* modulator can operate in either of two clock modes:
1) It can automatically lock to the incoming bit rates producing an output rate determined by the input stream. Power should be supplied by the model PS8 power supply module which also mounts into the DRMM12. The PS8 and DRMM12 are sold separately.

2) It can be set, from front panel controls, to output a desired symbol rate using an internally generated clock. In this mode, when the input bit rate is less than that needed to produce the set output symbol rate, null packets are added by the modulator.
The QAM signal must be up-converted to the desired channel with the Drake Up-Converter module, prior to transmission on the cable plant.

Specifications subject to change without notice or obligation.